

DEC 06 2007

Docket No. JCLA19679
US App. No. 10/574,528**IN THE CLAIMS:**

1. (cancelled)

2. (currently amended) ~~The circuit of claim 1,~~ In an amplification circuit using amplifying devices in an audio system, an input and output signal preservation circuit of an amplification circuit, comprising:

an input signal preservation unit that is connected with an input terminal of an amplification device of an amplification circuit capable of amplifying an AC input signal using a certain amplification device and outputting the same and increases an input resistance value with respect to the input signal and prevents an attenuation of the input signal; and

an output signal preservation unit that is connected with an output terminal of the amplification device and increases an output resistance value with respect to the amplification signal and prevents an attenuation of the amplification output signal for thereby compensating a voltage variation ratio with respect to the amplification output signal.

wherein said input signal preservation unit includes:

a bias resistor connected in parallel with an input terminal of the amplification device;

and

a reactor that is connected in series with the bias resistor and operates as an AC resistor and increases an input resistance value together with the bias resistor for thereby preventing a leakage of the AC input signal.

3. (currently amended) ~~The circuit of claim 1,~~ In an amplification circuit using amplifying devices in an audio system, an input and output signal preservation circuit of an amplification circuit, comprising:

an input signal preservation unit that is connected with an input terminal of an amplification device of an amplification circuit capable of amplifying an AC input signal using a

Docket No. JCLA19679
US App. No. 10/574,528

certain amplification device and outputting the same and increases an input resistance value with respect to the input signal and prevents an attenuation of the input signal; and

an output signal preservation unit that is connected with an output terminal of the amplification device and increases an output resistance value with respect to the amplification signal and prevents an attenuation of the amplification output signal for thereby compensating a voltage variation ratio with respect to the amplification output signal,

wherein said output signal preservation unit includes:

an amplification signal preservation part that includes a load resistor connected in parallel with an output terminal of the amplification device, and a first reactor that is connected in series with the load resistor and operates as an AC resistor and increases an output resistance value together with the load resistor for thereby preventing a leakage of the amplification output signal; and

a voltage compensation unit that includes a second reactor connected in parallel with a connection point of the load resistor and the first reactor and operating as an AC resistor for thereby increasing an output resistance value together with the load resistor and preventing a leakage of the amplification output signal, and a condenser that is connected in series with the second reactor and compensates the voltage variation ratio of the amplification output signal.

4. (currently amended) The circuit of claim 2, In an amplification circuit using amplifying devices in an audio system, an input and output signal preservation circuit of an amplification circuit, comprising:

an input signal preservation unit that is connected with an input terminal of an amplification device of an amplification circuit capable of amplifying an AC input signal using a certain amplification device and outputting the same and increases an input resistance value with respect to the input signal and prevents an attenuation of the input signal; and

Docket No. JCLA19679
US App. No. 10/574,528

an output signal preservation unit that is connected with an output terminal of the amplification device and increases an output resistance value with respect to the amplification signal and prevents an attenuation of the amplification output signal for thereby compensating a voltage variation ratio with respect to the amplification output signal.

wherein said input signal preservation unit includes:

a bias resistor connected in parallel with an input terminal of the amplification device;

and

a reactor that is connected in series with the bias resistor and operates as an AC resistor and increases an input resistance value together with the bias resistor for thereby preventing a leakage of the AC input signal.

wherein said output signal preservation unit includes:

an amplification signal preservation part that includes a load resistor connected in parallel with an output terminal of the amplification device, and a first reactor that is connected in series with the load resistor and operates as an AC resistor and increases an output resistance value together with the load resistor for thereby preventing a leakage of the amplification output signal; and

a voltage compensation unit that includes a second reactor connected in parallel with a connection point of the load resistor and the first reactor and operating as an AC resistor for thereby increasing an output resistance value together with the load resistor and preventing a leakage of the amplification output signal, and a condenser that is connected in series with the second reactor and compensates the voltage variation ratio of the amplification output signal.

5. (new) The circuit of claim 2, wherein the bias resistor and the reactor provide a bias at the input terminal of the amplification device.

6. (new) The circuit of claim 2, wherein the reactor is a low-frequency choke coil.